

52 ..... Square error calculating circuit  
53, 54 ..... Arithmetic unit  
55 ..... Integrator  
56 ..... Memory  
61 ..... Predictive coefficient memory  
62 ..... Corrected data memory  
63 ..... Error information memory  
64 ..... Comparing circuit  
65 ..... Control circuit  
71 ..... Receiver/reproducer  
72 ..... Separator  
73 ..... Classification blocking circuit  
74 ..... ADRC processing circuit  
75 ..... Classifying circuit  
76 ..... Predicting circuit  
77 ..... Predicted value calculation blocking circuit  
81 ..... Predicted value calculation blocking circuit  
82 ..... Predicting circuit  
91 ..... Learning blocking circuit  
92 ..... Teacher blocking circuit  
93 ..... ADRC processing circuit  
94 ..... Classifying circuit  
95 ..... Switch  
96 ..... Learning data memory  
97 ..... Counter  
98 ..... Teacher data memory  
99 ..... Calculating circuit

## DESCRIPTION OF REFERENCE NUMERALS

1	.....	Transmitting apparatus
2	.....	Recording medium
3	.....	Transmission path
4	.....	Receiving apparatus
11	.....	I/F
12	.....	ROM
13	.....	RAM
14	.....	CPU
15	.....	External memory
16	.....	Transmitter/recorder
21	.....	Compressor
22	.....	Local decoder
23	.....	Error calculator
24	.....	Decision unit
25	.....	Multiplexer
31	.....	Decimating circuit
32	.....	Correcting circuit
33	.....	Corrective value ROM
41	.....	Classification blocking circuit
42	.....	Predicted value calculation blocking circuit
43	.....	Classifying and adaptive processing circuit
44	.....	ADRC processing circuit
45	.....	Classifying circuit
46	.....	Adaptive processing circuit
51	.....	Blocking circuit